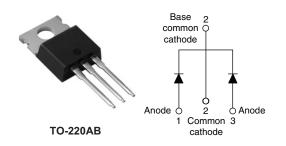


Vishay High Power Products

Schottky Rectifier, 2 x 5 A



SHA

PRODUCT SUMMARY			
I _{F(AV)}	2 x 5 A		
V _R	150 V		

FEATURES

- 175 °C T_J operation
- Center tap configuration
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	10	A		
V _{RRM}		150	V		
I _{FSM}	t _p = 5 μs sine	620	A		
V _F	5 Apk, T _J = 125 °C (per leg)	0.73	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	10CTQ150	UNITS	
Maximum DC reverse voltage	V _R	150 V		
Maximum working peak reverse voltage	V _{RWM}	150 V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per le		$I_{F(AV)}$ 50 % duty cycle at T _C = 155 °C, rectangular waveform		5	А
See fig. 5 per devic				10	
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	620	A
non-repetitive surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V_{RRM} applied	115	
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 0.30 \text{ A}, L = 150 \text{ mH}$		6.75	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.30	А

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	5 A	T 05 %O	0.93	V
		10 A	− T _J = 25 °C	1.10	
		5 A	- T _J = 125 °C	0.73	
		10 A		0.86	
Maximum reverse leakage current per leg See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.05	mA
		T _J = 125 °C		7	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.468	V
Forward slope resistance	r _t			28	mΩ
Maximum junction capacitance per leg	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		200	pF
Typical series inductance per leg	LS	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

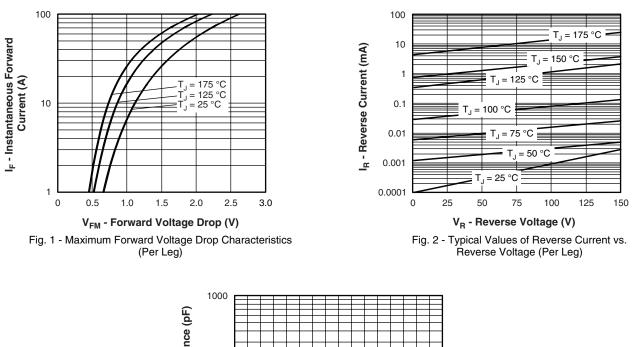
Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg				3.50	
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.75	°C/W
Typical thermal resistance, case to heatsink (only for TO-220))	R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
				0.07	oz.
Mounting torque	minimum			6 (5)	kgf · cm
	naximum			12 (10)	(lbf · in)
Marking device			Case style TO-220AB	10CT	Q150



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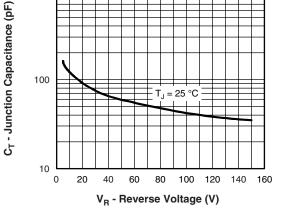


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

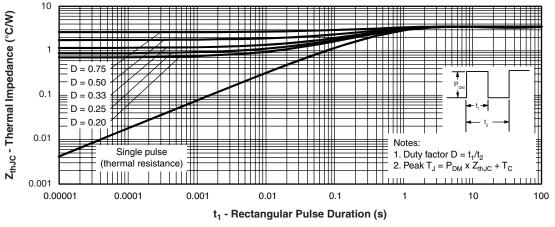
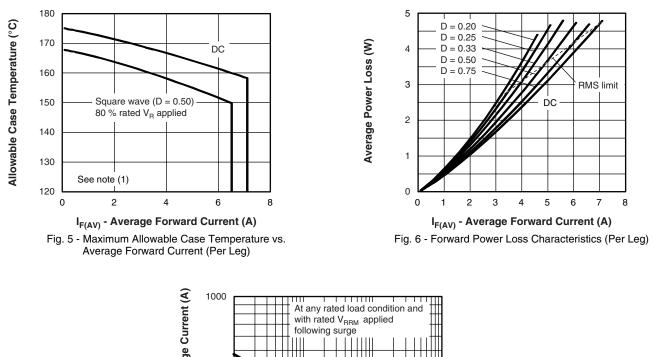


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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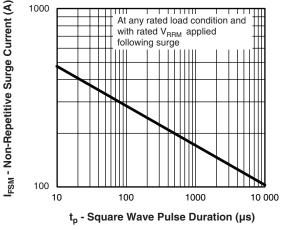


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

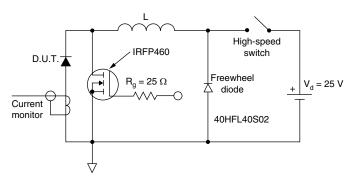


Fig. 8 - Unclamped Inductive Test Circuit

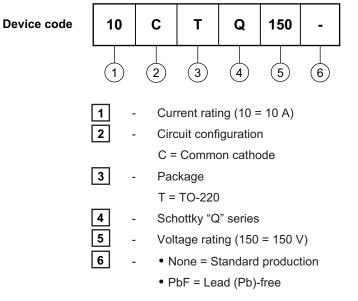
Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 10 V$



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ORDERING INFORMATION TABLE



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95222			
Part marking information	http://www.vishay.com/doc?95225		



Vishay

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